

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	497	(esc ((electrostatic electro\$static (electro adj static)) adj (chuck\$3 clamp\$3 hold\$3)) chuck\$3) same ((substrate wafer die board chip plasma (gallium ADJ arsenide ADJ chip) (hybrid ADJ chip) microchip microcircuit motherboard (neural ADJ network ADJ chip) PCB (printed ADJ circuit ADJ board) (semiconductor ADJ chip) superchip transputer (wafer ADJ chip)) near3 (contact\$3 touch\$3 top) near3 (base block support))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/30 20:31
S2	15	(esc ((electrostatic electro\$static (electro adj static)) adj (chuck\$3 clamp\$3 hold\$3)) chuck\$3) same ((substrate wafer die board chip plasma (gallium ADJ arsenide ADJ chip) (hybrid ADJ chip) microchip microcircuit motherboard (neural ADJ network ADJ chip) PCB (printed ADJ circuit ADJ board) (semiconductor ADJ chip) superchip transputer (wafer ADJ chip)) adj (contact\$3 touch\$3 top) adj (base block support))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/30 21:03
S3	9	(esc ((electrostatic electro\$static (electro adj static)) adj (chuck\$3 clamp\$3 hold\$3)) chuck\$3) same ((substrate wafer die board chip plasma (gallium ADJ arsenide ADJ chip) (hybrid ADJ chip) microchip microcircuit motherboard (neural ADJ network ADJ chip) PCB (printed ADJ circuit ADJ board) (semiconductor ADJ chip) superchip transputer (wafer ADJ chip)) adj (contact\$3 touch\$3 top) adj (base block support)) and (seal\$3 ring)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/03 19:29

## EAST Search History

S4	7	(esc ((electrostatic electro\$static (electro adj static)) adj (chuck\$3 clamp\$3 hold\$3)) chuck\$3) same ((substrate wafer die board chip plasma (gallium ADJ arsenide ADJ chip) (hybrid ADJ chip) microchip microcircuit motherboard (neural ADJ network ADJ chip) PCB (printed ADJ circuit ADJ board) (semiconductor ADJ chip) superchip transputer (wafer ADJ chip)) adj (contact\$3 touch\$3 top) adj (base block support)) and (seal\$3 ring) and (inner internal center central) and (outter external periphery perypheral perimeter)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/03 19:31
S5	7	(esc ((electrostatic electro\$static (electro adj static)) adj (chuck\$3 clamp\$3 hold\$3)) chuck\$3) same ((substrate wafer die board chip plasma (gallium ADJ arsenide ADJ chip) (hybrid ADJ chip) microchip microcircuit motherboard (neural ADJ network ADJ chip) PCB (printed ADJ circuit ADJ board) (semiconductor ADJ chip) superchip transputer (wafer ADJ chip)) adj (contact\$3 touch\$3 top) adj (base block support)) and (seal\$3 ring) and (inner internal center central) and (outer external periphery peripheral perimeter)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/03 19:33
S6	705	361/234.CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/03 19:33